

* Insert two pin connector wire from Green Power Supply into Connector J1.

VESA Bus Connector

The cache system board provides two high-performance VESA bus connectors, SL14 and SL15, for use with VESA peripherals. These connectors can be utilized for one Local Bus Master and one Local Bus Slave, either (SL14) or (SL15).

The following tables give the pin assignments for SL14 and SL15. Side A of the connector are pin outs on the board's component side while Side B are pin outs on the board's solder side. Jumpers JV2 and JV3 give more information on settings on the mainboard and the VL-bus controller:

JUMPER	PIN DEFINITION
JV2	CPU Speed Select 1-2 > 33 MHz 2-3 5.33 MHz
JV3	High Speed Write Select 1-2 One wait write 2-3 Zero wait write (default)

→ **NOTE : The two VESA Local Bus slot can accommodate either one VESA Master with one VESA Slave or two VESA Slaves.**

CONNECTOR	SIDE A : PINS AND PIN OUTS	SIDE B : PINS AND PIN OUTS
01	DAT01	DAT00
02	DAT05	DAT02
03, 10, 17, 24, 35, 43,	Ground	DAT04
51	DAT05	DAT06
04	DAT05	DAT08
05	DAT09	Ground
06	DAT09	Ground
07	DAT11	DAT10
08	DAT13	DAT12
09	DAT17	DAT12
11	DAT17	VCC
12, 27, 40, 53	VCC	DAT14
13	DAT19	DAT16
14	DAT21	DAT20
15	DAT23	DAT22
16	DAT25	DAT24
18	DAT29	DAT26
19	DAT29	DAT28
20	DAT31	DAT30
21	ADDR0	ADDR31
22	ADDR2	ADDR29
23	ADDR6	ADDR27
24	ADDR24	ADDR25
25	ADDR24	ADDR25
26	ADDR22	ADDR23
27	ADDR20	ADDR21
28	ADDR18	ADDR19
30	ADDR16	ADDR17
31	ADDR14	ADDR15
32	ADDR12	ADDR13
33	ADDR10	ADDR11
34	ADDR8	ADDR9
35	ADDR6	ADDR7
36	ADDR4	ADDR5
37	WBCK#	ADDR3
38	WBCK#	ADDR0
39	BE0#	NC
40	BE1#	NC
41	BE2#	RESET#
42	BE3#	D/C#
43	BE4#	D/C#
44	BE5#	D/C#
45	AD0#	M/IO#
48	LDY#	W/R#
49	LDEV0#	RDY#
50	LREQ#	RDY#
51	LGNTE	RDY#
52	ID2, 3, 4	BRDY#
54, 55, 56	LENS	ID0, 1
57	LENS	LDLKO
58	LENS	LBST#

Table 2-5. Local Bus Connector Pin Assignment (Continued)