

CONNECTOR	SIDE A - PINS AND PIN OUTS	SIDE B - PINS AND PIN OUTS
	01 DAT01	01 DAT00
	02 DAT03	02 DAT02
	03, 10, 17, 24, 35, 43, 51	03 Ground
	04 DAT05	04 DAT06
	05 DAT08	05 DAT08
	06 DAT09	06, 14, 22, 29, 38, 49, 55
	07 DAT10	07 Ground
	08 DAT13	08 DAT10
	09 DAT15	09, 20, 32, 57
	10 DAT17	10 VCC
	11, 12, 27, 40, 53	11 VCC
	13 DAT19	12 DAT16
	14 DAT21	13 DAT18
	15 DAT23	14 DAT20
	16 DAT25	15 DAT22
	17 DAT27	16 DAT24
	18 DAT29	17 DAT26
	19 DAT31	18 DAT28
	20 DAT33	19 DAT30
	21 ADR30	20 ADR31
	22 ADR28	21 ADR29
	23 ADR26	22 ADR27
	24 ADR24	23 ADR25
	25 ADR22	24 ADR23
	26 ADR20	25 ADR21
	27 ADR18	26 ADR19
	28 ADR16	27 ADR17
	29 ADR14	28 ADR15
	30 ADR12	29 ADR13
	31 ADR10	30 ADR11
	32 ADR08	31 ADR09
	33 ADR06	32 ADR07
	34 ADR04	33 ADR05
	35 ADR02	34 ADR03
	36 WBAC#	35 ADR04
	37 BE0#	36 ADR05
	38 BE1#	37 ADR06
	39 BE2#	38 ADR07
	40 BE3#	39 ADR08
	41 BE4#	40 ADR09
	42 BE5#	41 ADR10
	43 BE6#	42 ADR11
	44 BE7#	43 ADR12
	45 BE8#	44 ADR13
	46 BE9#	45 ADR14
	47 BE10#	46 ADR15
	48 BE11#	47 ADR16
	49 BE12#	48 ADR17
	50 BE13#	49 ADR18
	51 BE14#	50 ADR19
	52 BE15#	51 ADR20
	53 BE16#	52 ADR21
	54 BE17#	53 ADR22
	55 BE18#	54 ADR23
	56 BE19#	55 ADR24
	57 BE20#	56 ADR25
	58 LEADS#	57 ADR26
		58 ADR27

Table 2-5. Local Bus Connector Pin Assignment

486-GVT-2

Chapter 3

Memory Subsystem

The 486-GVT-2 is equipped with the memory necessary for running all your applications. Memory comes in the form of DRAM (SIMMs) and cache SRAM. This chapter describes these two kinds of memory and gives instructions on how to install each kind on the mainboard.

Memory Locations

The board layout below shows the locations of the DRAM memory banks and the cache SRAM:

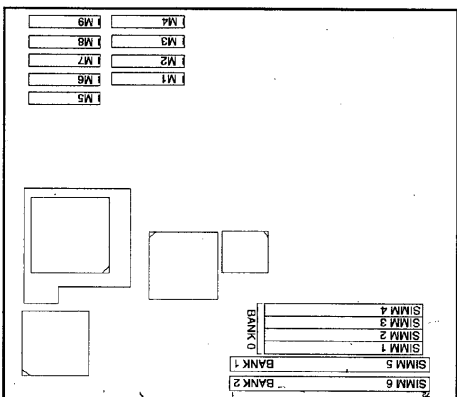


Figure 3-1. Cache and Memory Locations

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